

GOA UNIVERSITY  
 COLLEGEWISE STUDENT RESULT REGISTER FOR VTH SEMESTER EXAM

\*\* RC 2007-08 \*\*  
 EXAM YEAR : MAY-2015

SEAT NO.	P.R. NO.	ARE CD.	PRO VI.	SEX	NAME OF THE CANDIDATE	PAPER DESCRIPTION	THEORY MAX. 100 MIN. 040	SESS 25	TOTAL 125 50	PRACTICAL 50 OR 25 20 OR 10	DRAL 50 OR 25 20 OR 10	REMARKS MAX. 850 MIN. 340
0042	201204014	(E)	N	M	ASHIS SHARMA	PROBABILITY THEORY & RANDOM PROCESSES	040		010	050+		
					CONTROL SYSTEM ENGINEERING	037	#3	007	044E	#6		
					COMMUNICATION ENGINEERING I	040		007	047E	#3	034+	
					MICROPROCESSORS	040		005	045E	#5	031+	
					DIGITAL SIGNAL PROCESSING	053		010	063+			
					TRANSMISSION LINES & WAVEGUIDES	000		008	008A			
												322 #14 F
0043	201008299	(E)	N	M	BARVE UDAY DEVIDAS	PROBABILITY THEORY & RANDOM PROCESSES	029		008	037F		
					CONTROL SYSTEM ENGINEERING	035	#5	010	045+	#5		
					COMMUNICATION ENGINEERING I	035	#5	009	044+	#6	030+	
					MICROPROCESSORS	040		005	045+	#5	020+	
					DIGITAL SIGNAL PROCESSING	055		010	065+			
					TRANSMISSION LINES & WAVEGUIDES	057		013	070+			
												356 #16 F
0044	201203999	(E)	N	M	D ROMANESH	PROBABILITY THEORY & RANDOM PROCESSES	000		009	009A		
					CONTROL SYSTEM ENGINEERING	040		011	051+			
					COMMUNICATION ENGINEERING I	040		008	048+	#2	034+	
					MICROPROCESSORS	041		010	051+		036+	
					DIGITAL SIGNAL PROCESSING	026	#14	004	030+#14#6			
					TRANSMISSION LINES & WAVEGUIDES	040		012	052+			
												311 #14#08 A
0045	201104812	(E)	N	M	DESAI PARAG RAJARAM	PROBABILITY THEORY & RANDOM PROCESSES	049		005	054P		
					CONTROL SYSTEM ENGINEERING	038	#2	011	049+	#2		
					COMMUNICATION ENGINEERING I	045		003	048+	#2	035+	
					MICROPROCESSORS	048		007	055+		021+	
					DIGITAL SIGNAL PROCESSING	049		012	061+			
					TRANSMISSION LINES & WAVEGUIDES	040		012	052+			
												375 #04 P
0046	201305727	(E)	N	F	FERNANDES CATHERINE MARIA	PROBABILITY THEORY & RANDOM PROCESSES	000		008	008A		
					CONTROL SYSTEM ENGINEERING	035	#5	014	049+	#5		
					COMMUNICATION ENGINEERING I	035	#5	010	045+	#5	023+	
					MICROPROCESSORS	044		014	058+		033+	
					DIGITAL SIGNAL PROCESSING	040		014	054+			
					TRANSMISSION LINES & WAVEGUIDES	046		016	062+			
												332 #10 A
0047	200801315	(E)	N	M	GADNKAR KISAN DHAKALU	PROBABILITY THEORY & RANDOM PROCESSES	052		004	056+		
					CONTROL SYSTEM ENGINEERING	051		012	063+			
					COMMUNICATION ENGINEERING I	040		006	046+	#4	030+	
					MICROPROCESSORS	043		007	050P		023+	
					DIGITAL SIGNAL PROCESSING	047		011	058+			
					TRANSMISSION LINES & WAVEGUIDES	035	#5	016	051+	#5		
												377 #09 P

SEAT P.R. NO.	NO. ARE PRO	SEX	NAME OF THE CANDIDATE	THEORY	SESS	TOTAL	PRACTICAL	ORAL	REMARKS
NO.	CD. VI.			MAX. 100	25	125	50 OR 25	50 OR 25	
	PAPER DESCRIPTION			MIN. 040		50	20 OR 10	20 OR 10	MAX. 850 MIN. 340
0048	201104025	(E) N	M JOSHI ADITYA KALIDAS						
	PROBABILITY THEORY & RANDOM PROCESSES			000	016	016A			
	CONTROL SYSTEM ENGINEERING			000	022	022A			
	COMMUNICATION ENGINEERING I			008	009	017F		000A	
	MICROPROCESSORS			052	013	065+		000A	
	DIGITAL SIGNAL PROCESSING			046	015	061+			
	TRANSMISSION LINES & WAVEGUIDES			035	\$5	010	045+	\$5	226 \$05 F
0049	201008572	(E) N	M K. J. DEEPAK						
	PROBABILITY THEORY & RANDOM PROCESSES			064	001	065+			
	CONTROL SYSTEM ENGINEERING			050	014	064+			
	COMMUNICATION ENGINEERING I			049	006	055+		030+	
	MICROPROCESSORS			066	014	080+		027+	
	DIGITAL SIGNAL PROCESSING			040	011	051+			
	TRANSMISSION LINES & WAVEGUIDES			035	\$5	013	048P	\$5	420 \$05 P
0050	201008279	(E) N	M NAIK VISHNU ALIAS VIRAJ RAMKRISHNA						
	PROBABILITY THEORY & RANDOM PROCESSES			064	006	070+			
	CONTROL SYSTEM ENGINEERING			051	012	063+			
	COMMUNICATION ENGINEERING I			013	005	018F		024+	
	MICROPROCESSORS			006	003	009F		025+	
	DIGITAL SIGNAL PROCESSING			051	012	063+			
	TRANSMISSION LINES & WAVEGUIDES			039	\$1	014	053+	\$1	325 \$01 F
0051	200703561	(E) N	M PALNI AMEY VINAYAK						
	PROBABILITY THEORY & RANDOM PROCESSES			040	006	046+	\$4		
	CONTROL SYSTEM ENGINEERING			040	010	050+			
	COMMUNICATION ENGINEERING I			044	005	049+	\$1	021+	
	MICROPROCESSORS			000	005	005A		024+	
	DIGITAL SIGNAL PROCESSING			038	\$2	010	048+	\$2	
	TRANSMISSION LINES & WAVEGUIDES			037	\$3	013	050+	\$3	293 \$10 A
0052	201203914	(E) N	M SHIRODKAR NIKHIL PRATAP						
	PROBABILITY THEORY & RANDOM PROCESSES			044	007	051+			
	CONTROL SYSTEM ENGINEERING			047	013	060+			
	COMMUNICATION ENGINEERING I			037	\$3	011	048+	\$3	041+
	MICROPROCESSORS			045	011	056+		038+	
	DIGITAL SIGNAL PROCESSING			020	011	031F			
	TRANSMISSION LINES & WAVEGUIDES			000	017	017A			342 \$03 F
0053	201203990	(E) N	M SIDDHARTH S PILLAI						
	PROBABILITY THEORY & RANDOM PROCESSES			068	019	087+			
	CONTROL SYSTEM ENGINEERING			055	010	065+			
	COMMUNICATION ENGINEERING I			040	008	048+	\$2	022P	
	MICROPROCESSORS			056	007	063+		032+	
	DIGITAL SIGNAL PROCESSING			053	013	066+			
	TRANSMISSION LINES & WAVEGUIDES			054	016	070+			453 \$02 P

GOA UNIVERSITY  
 I 04 ELECTRONICS & TELECOMM.  
 COLLEGE : GOA COLLEGE OF ENGINEERING

ENGINEERING EXAMINATION SYSTEM  
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						MAX. 100	MIN. 040			25	125	50 DR 25	20 DR 10		50 DR 25
PAPER DESCRIPTION															
0054	201203917	(E)	N	M	SINAI VELINGKAR KAPIL ATMARAM										
					PROBABILITY THEORY & RANDOM PROCESSES	000		001	001A						
					CONTROL SYSTEM ENGINEERING	000		008	008A						
					COMMUNICATION ENGINEERING I	000		007	007A			038+			
					MICROPROCESSORS	036	\$4	010	046+	\$4		035+			
					DIGITAL SIGNAL PROCESSING	000		010	010A						
					TRANSMISSION LINES & WAVEGUIDES	000		009	009A						
													154	#04 A	
0055	200907866	(E)	N	M	VAZ VELEND KARAL										
					PROBABILITY THEORY & RANDOM PROCESSES	040		006	046+	\$4					
					CONTROL SYSTEM ENGINEERING	036	\$4	012	048E	\$4					
					COMMUNICATION ENGINEERING I	043		006	049+	\$1		030+			
					MICROPROCESSORS	000		010	010A			020+			
					DIGITAL SIGNAL PROCESSING	040		011	051+						
					TRANSMISSION LINES & WAVEGUIDES	035	\$5	014	049E	\$5					
													303	#14 F	

READ BY

*z*

CHECKED BY

*Bomalo*

DECLARED ON

*29/7/2015*

ASST. REGISTRAR-E (PROF)

*Agnew*

CONTROLLER OF EXAMINATION

*Macedo*  
*29/7/15*

REGISTRAR

*Vkand*